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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
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FOREIGN PATENT DOCUMENTS

	Document Number	Date MM/YYYY	Country	Inventor Name	English Abstract		Translation Readily Available	
					Enclosed	No	Enclose	No
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OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

TD	QR	PATENT ABSTRACTS OF JAPAN, "MULTILAYERED WIRING BOARD" JP 11-126978, May 11, 1999, Akiya.Fujisaki et al		
	RR	PATENT ABSTRACTS OF JAPAN " METHOD FOR MANUFACTURING MULTILAYER PRINTED WIRING BOARD ", JP 10-013024, January 1, 1998, Satoshi Nakamura		
	SR	PATENT ABSTRACTS OF JAPAN, "ELECTRODE STRUCTURE AND THE MANUFACTURING METHOD", JP 54-157296, December 12, 1979		
TD	TR	PATENT ABSTRACTS OF JAPAN, " MULTILAYER ELECTRONIC PART AND MANUFACTURE THEREOF ", JP 8-241827, September 17, 1996		
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Date Considered: 12/27/02

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